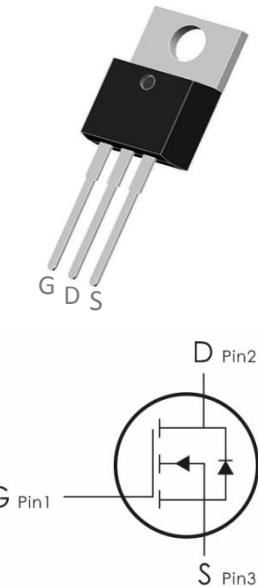


## Description:

This N-Channel MOSFET uses advanced SGT technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.



## Features:

- 1)  $V_{DS}=100V, I_D=40A, R_{DS(on)}<20m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.

## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>1)</sup> , $T_c=25^\circ C$	40	A
$I_{D, pulse}$	Pulsed drain current <sup>2)</sup> , $T_c=25^\circ C$	98	A
$P_D$	Power dissipation <sup>3)</sup> , $T_c=25^\circ C$	96	W
$E_{AS}$	Single pulsed avalanche energy <sup>5)</sup>	65	mJ
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{eJC}$	Thermal Resistance,Junction to Case	1.3	$^\circ C/W$
$R_{eJA}$	Thermal Resistance,Junction to Ambient <sup>4)</sup>	62	

**Electrical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	100	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1.4	---	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	13.8	20	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=7\text{A}$	---	17.4	26	
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=0\text{V}, f=100\text{KHz}$	---	1000	---	pF
$C_{\text{oss}}$	Output Capacitance		---	180	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	9.5	---	
<b>Switching Characteristics</b>						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=5\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=10\Omega$	---	16.6	--	ns
$t_r$	Rise Time		---	3.8	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	75.5	---	ns
$t_f$	Fall Time		---	46	---	ns
$Q_g$	Total Gate Charge		---	16.2	---	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=50\text{V}, I_{\text{D}}=5\text{A}$	---	2.8	---	nC
$Q_{\text{gd}}$	Gate-Drain Charge		---	4.1	---	nC
$V_{\text{plateau}}$	Gate plateau voltage		---	3	---	V
<b>Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Source-Drain Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=12\text{A}$	---	---	1.3	V

<b>trr</b>	Reverse Recovery Time	$V_R=50\text{ V}, I_S=5\text{ A},$ $di/dt=100\text{ A}/\mu\text{ s}$	---	49	---	Ns
<b>qrr</b>	Reverse Recovery Charge		---	61.8	---	nc
<b>I<sub>rmm</sub></b>	Peak reverse recovery current		---	2.4	---	A

**Notes:**

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_a=25\text{ }^\circ\text{C}$ .
- 5)  $V_{DD}=50\text{ V}, V_{GS}=10\text{ V}, L=0.3\text{ mH}$ , starting  $T_j=25\text{ }^\circ\text{C}$ .

Typical Characteristics: ( $T_c=25\text{ }^\circ\text{C}$  unless otherwise noted)

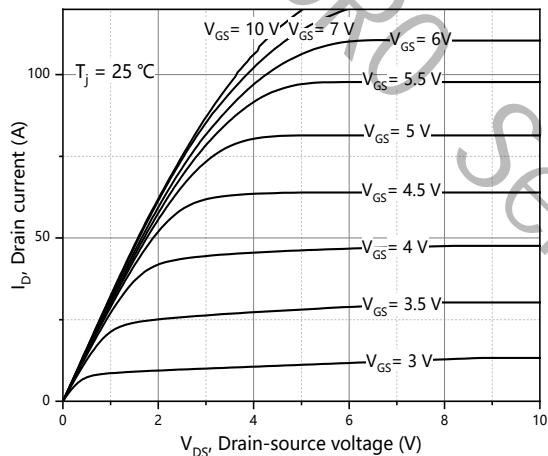


Figure 1. Typ. output characteristics

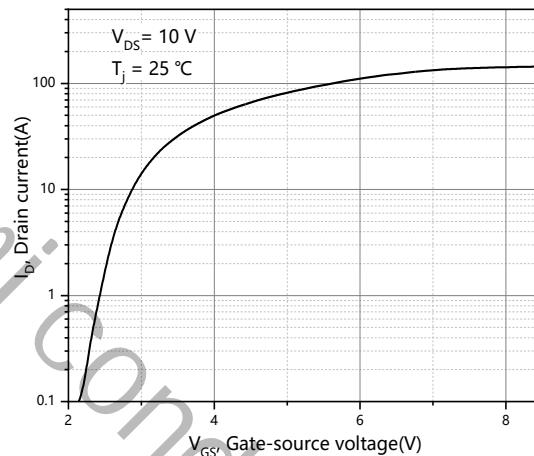


Figure 2. Typ. transfer characteristics

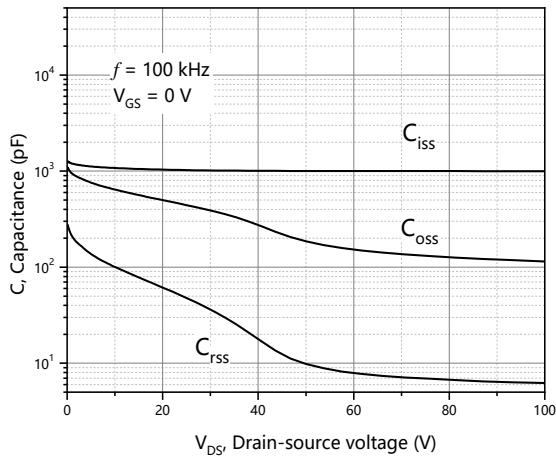


Figure 3. Typ. capacitances

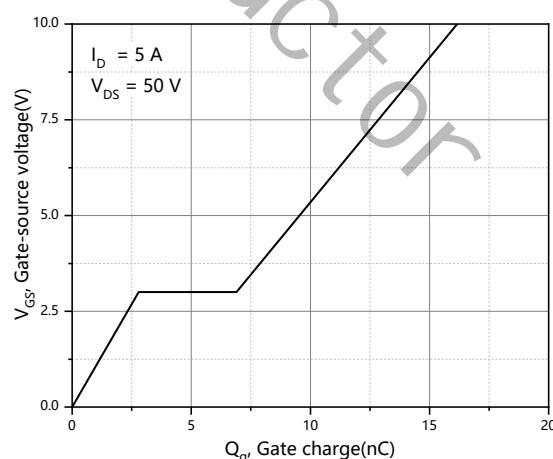


Figure 4. Typ. gate charge

